

REMARKS

Reconsideration and allowance are respectfully requested. Claims 1-23 are pending. The examiner's latest office action is virtually identical to his April 11, 2003 office action, with a few exceptions that need not be discussed at this time. Independent claims 1, 5, 10, 11 and 20 have been amended by moving the phrase "an electronics assembly engineering system" to a position in the claims that is after the word "comprising" to emphasize the fact that such a system is an essential requirement and limitations of those claims.

Claims 16 and 20 stand rejected under 102(e) under Choy (US 6,321,374). The Examiner states on page 3 that Choy discloses "manipulation of data structures, the plurality of functions excluding a save-as function." The Examiner's statement: (1) fails to point to any portion of Choy where the alleged disclosure appears and (2) is not accurate. In fact, Choy does not disclose "providing a plurality of functions for either or both of naming and manipulation of data structures, the plurality of functions excluding a save-as function" as recited in Claims 16 and 20. For that reason alone, the rejection of Claims 16 and 20 and the rejection of the claims that depend therefrom is traversed.

Still further, with regard to claim 20, Choy does not teach a method for enabling naming and manipulating functions for data structures in a computer subsystem comprising an electronics assembly engineering system, which is now recited in claim 20. Choy relates to software for a digital library. Thus, the rejection of claim 20 and the claims that depend therefrom is improper and should be withdrawn.

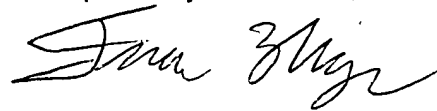
Claims 1-2, 5, 9-13, 17-19 and 21-23 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Bolnick et al. in view of Choy. This rejection is respectfully traversed. The Examiner states that "the 'electronics assembly engineering subsystem' is not given patentable weight." Applicant disagrees. In any case, independent claims 1, 5, 10 and 11 as amended clearly recite a system or media "comprising an electronics assembly engineering system". In that regard, Bolnick et al. does not teach or suggest the computer subsystem in an electronics assembly engineering system as claimed.

Furthermore, each independent claim requires that the "save-as" function for data structures be excluded. In fact, Bolnick et al. discloses and includes Save and Save As functions (column 17, lines 20-28) but does not teach how those functions

could be excluded; Bolnick et al. teaching of Save and Save As functions teaches away from Applicant's invention of excluding Save and Save As functions. To argue that Bolnick et al. merely discloses those functions as options does not support the argument that Bolnick addressed how to exclude them as recited by Applicant's claims. For these reasons, the Section 103 rejection of claims 1, 5, 10-12 should be withdrawn. Likewise, the rejection of the claims that depend from those claims (i.e., the rejection of claims 3-4, 7-8 and 14-15 under 35 U.S.C. 103(a) over Bolnick et al. in view of Choy and Ferrel et al.) should be withdrawn.

All objections and rejections having been addressed, it is respectfully submitted that the subject application is in condition for allowance and a Notice to that effect is earnestly solicited.

Respectfully submitted,



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